Advt. No: JK/2830/2025   **Date: 10/05/2025**

Advertisement for Ph.D. Admissions under Visvesvaraya PhD Scheme, for Electronics and IT, Phase-II, Digital India Corporation, MietY, GoI

The University of Allahabad invites applications for Full -Time Ph.D. admissions under the Visvesvaraya Ph.D. Scheme for Electronics and IT: Phase-II for the Session 2025-26. The available seats and area of coverage are as follows:

Table 1		
Department	**Area of Coverage**	**No. of Seats (Full-Time)**
Electronics and Communication, University of Allahabad	• **Electronics System Design & Manufacturing (ESDM)** • **Information Technology/IT Enabled Services (IT/ ITES)**	**03**
• **Eligibility: Bachelor's Degree (B.E./B.Tech) and Master's Degree (M.E./M.Tech) in relevant branch of Engineering/Technology (Electronics and Communication Engineering/Computer Science/Information Technology) with marks not below 60% or CPI 6.5 (In case of SC/ST/PwD marks not below 55% or CPI 6.0).** • **Age Limit: The candidates must not be more than 32 years of age on the last date of the application. The age relaxation shall be applicable as per GOI norms.** • **The reservation under the SC, ST, OBC (Non-Creamy Layer), EWS and PwD categories will be admissible as per the GoI norms.**		

Salient Features of the Visvesvaraya PhD Scheme (Full-Time Candidates)

Candidates selected under this scheme will be enrolled at the supported institution (University of Allahabad) and will be governed by both the **institution's rules** and the **Terms & Conditions of the Visvesvaraya PhD Scheme: Phase-II**.

📌 **Financial Support for Full-Time PhD Candidates:**
- **Fellowship**: Rs 38,750/- per month (I & II year); Rs 43,750/- (III to V year).
- **Rent Reimbursement**: For candidates not provided hostel accommodation, rent reimbursement up to **16% of the fellowship amount** is available, as per Government of India norms for Class Y cities.
- **Research Contingency Grant**: ₹1,20,000 per year for research-related expenses throughout the PhD duration
- **International Conference Support**: Financial support up to **₹1.5 lakh per candidate** to attend international conferences.
- **Visit to International Labs (From 3rd Year Onwards)**: Selected candidates who meet the required eligibility criteria will be supported to visit labs abroad as per the norms of Ph.D. scheme.

For more details regarding Visvesvaraya Ph.D. scheme, interested candidates may visit the web portal: http://phd.digitalindiacorporation.in/

Admission Process

1. The admission process has two stages: Stage I and Stage II.
   a. **Stage I** is a screening test.
      - It will be held **only at the University of Allahabad campus**, in **offline mode**.
      - The **screening test will be based on the syllabus of GATE 2025 (Electronics and Communication Engineering (ECE)/Computer Science and Information Technology (CS))**. For detailed syllabus, please visit https://gate2025.iitr.ac.in/exam-papers-and-syllabus.html.
      - Based on the test results, a list of candidates selected for the **Personal Interview** will be announced.
   b. **Stage II** is the **Personal Interview**,
      - Conducted by a panel of experts,
      - And may take place **on the same day or next day of Stage I test**.
2. The selection of the candidate will be subject to the recommendation of the interview panel & approval of Competent Authority.
3. The university reserves the right to not fill all the seats, in case suitable candidates are not found available.

Application Process

1. Interested and eligible candidates are invited to apply by submitting a **duly completed application form (Annexure-1) in the prescribed format (attached), along with receipt of submitted requisite fee, comprehensive CV and self-attested photocopies of all supporting documents** be **sent by Indian post, to The Head of the Department, Department of Electronics and Communication, Science Faculty, University of Allahabad, Prayagraj-211002, U.P. India**. The hard copy **envelop should clearly be superscribed with "Application for Ph.D. Admissions under Visvesvaraya PhD Scheme, for Electronics and IT, Phase-II, DIC, MietY at University of Allahabad"**. The hard copy of the application from along with all supporting documents must be received **no later than 30/05/2025.**
2. **Application Fee** (non-refundable): **Rs.1000/- for General / EWS / OBC category & Rs.500/- for SC / ST / PWD category.** The fee must be submitted by online in **the following bank account and attach the fee receipt with the Application form.**
   Name of Bank: Punjab National Bank, Colonelganj, Allahabad
   Name of Account Holder: JK Institute PROF FEE AC
   Account No: 1001000100222313
   IFSC Code: PUNB0100100

3. In addition to the hard copy, candidates must fill the **google form** **https://forms.gle/U6TczESLdS4w1k717** and upload the completed application-including the filled form, CV and supporting documents. The google form must be submitted no later than **30/05/2025**.

4. Only shortlisted candidates will be contacted via email for further selection procedures.

5. Please note that no TA/DA will be provided for attending the screening test/interview.

6. The candidates are advised to visit the official website of the University of Allahabad at **https://www.allduniv.ac.in** for updates.

7. The working candidates must produce a No Objection Certificate (NOC) from their current employer and attach the NOC with the application form.

8. Only matriculation/SSC certificate/pass certificate issued by the concerned educational board will be considered as proof of date of birth. No other document will be accepted for verification of date of birth.

9. Candidates belonging to SC/ST/OBC category should submit proper caste certificate as per the proforma of Govt. of India. The certificate shall not be more than 06 months old in case of SC/ST/OBC/EWS.

10. The University reserves the right to Revise/Reschedule/Cancel/Suspend the admission process without assigning any reason. The decision of the University shall be final and no appeal in this regard shall be entertained.

TENTATIVE SCHEDULE FOR ADMISSION

Start date of Application	10/05/2025
Last date of Application	30/05/2025
Announcement of Shortlisted Candidates for Screening Test	06/06/2025 (Tentative)
Date of Screening Test	To be announced
Date of Interview	To be announced
Result Declaration	To be announced
Date of Registration, Fee Payment & Physical Reporting of Candidate for admissions	To be announced

Nodal Officer,
Visvesvaraya PhD Scheme,
University of Allahabad

Annexure-1

Application for Ph.D. Admissions under Visvesvaraya PhD Scheme for Electronics and IT, Phase-II, DIC, MietY, GoI, UoA
Last Date of Application: 30/05/2025

1. Name of the Candidate (In BLOCK LETTERS):
2. Father's Name:
3. Date of Birth (DOB):
4. Sex (M/F):
5. Category (General/OBC/SC/ST):
6. Nationality:
7. Mobile No.:
8. Email address:
9. Address:

Correspondence:	Permanent:

10. Academic/Professional Qualifications (Attach the copy of proof, marksheet/grade card, etc.):

Educational Qualifications	Institute/University/ Board	Discipline /Field	Year of Passing	Class/ Division	% of Marks/ CGPA
10th or equivalent					
12th or equivalent					
B.Tech.					
M.Tech.					
Any other					

11. GATE/NET Qualification (Yes/No) (If Yes, attach a copy of the proof):
Appeared Year: GATE/NET Rank: GATE/NET Score:

12. Details of work Experience, if any (Separate sheet may be attached along with the copy of proof):

13. Previous Research work and Publications, if any (Separate sheet may be attached along with the copy of Proof):

*(Attach latest category certificate as per government norms)

DECLARATION

I   hereby declare that all the statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be canceled.

Date: Signature of the Applicant